

[54] TRANSISTOR AMPLIFIER

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[57] ABSTRACT

An amplifier comprising of two MOS-transistors Q_1 and Q_2 and a resistor R_1 connected as a two-pole between a DC power source, PS_1 , and a load Z_L ; in order to avoid harmful influences on the amplifier from the load current, and to obtain a simple control unit for this purpose:

- (a) the emitters (sources) of Q_1 and Q_2 form one pole of the two-pole, and one side of R_1 is connected in series with the collector (drain) of Q_1 , and the other side of R_1 together with the collector (drain) of Q_2 form the other pole of the two-pole,
- (b) the resistance of R_1 is of the same magnitude as Z_L ,

- (c) a mainly constant-DC-voltage-generating network, V_B , is connected between the gates of Q_1 and Q_2 , with a polarity causing Q_2 to conduct less than Q_1 , and a voltage causing Q_2 not to conduct before Q_1 conducts or aims to conduct the full rated current, i.e. a current of the same magnitude as the output current,

- (d) the driver stage is connected to the gate of Q_1 or to the gate of Q_2 or at some other point in the constant-DC-voltage-generating network, V_B .

3 Claims, 5 Drawing Figures

